

FIG. 1a

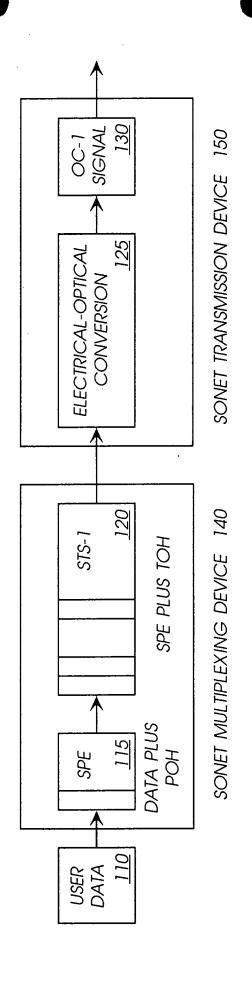


FIG. 1b

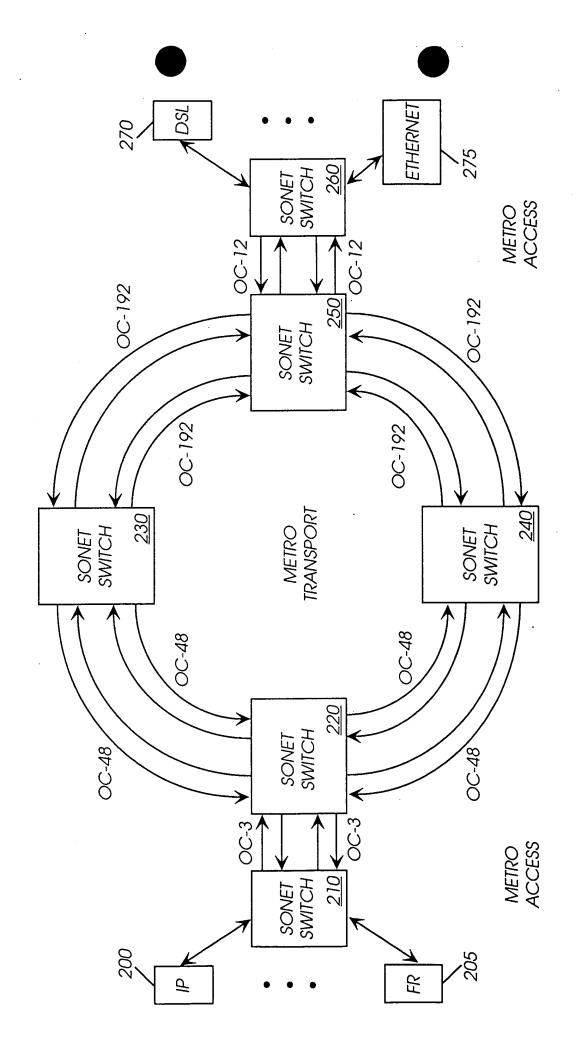


FIG. 2

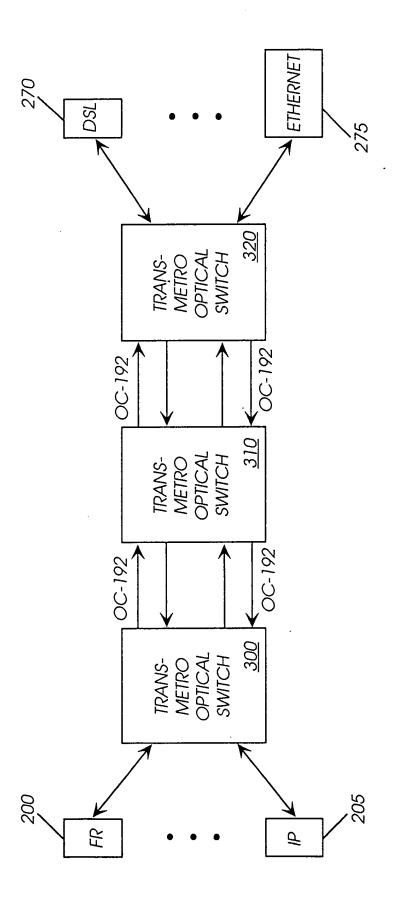
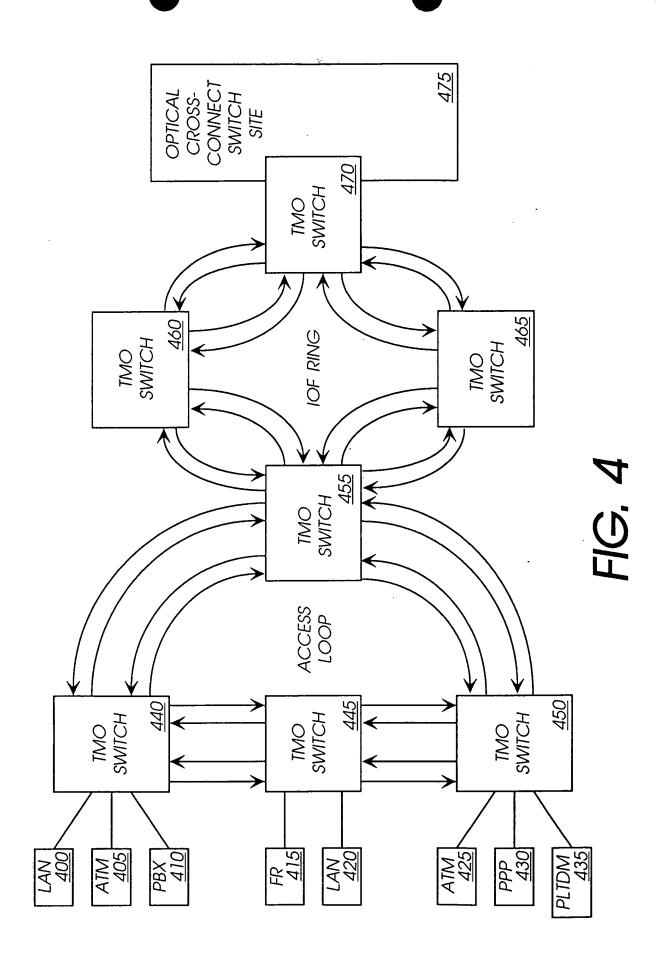


FIG. 3



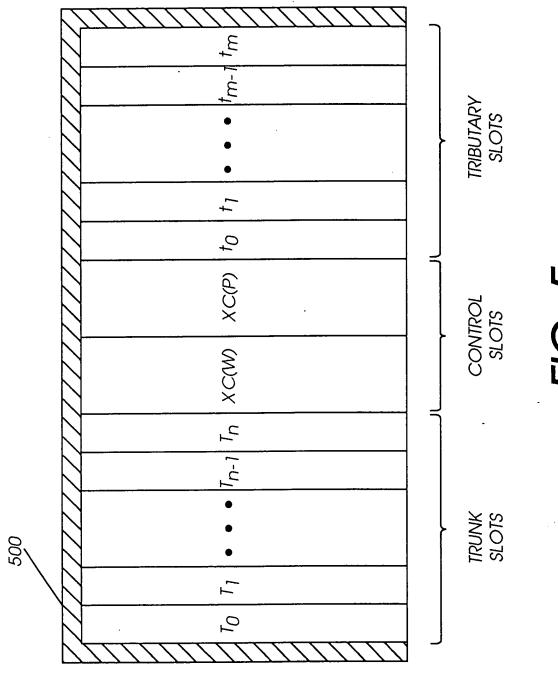
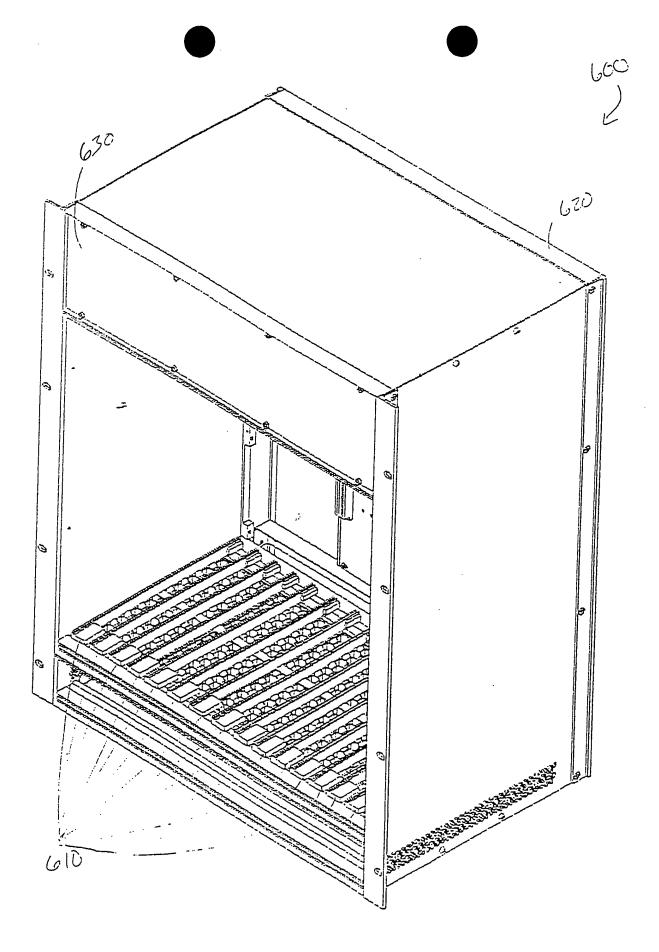
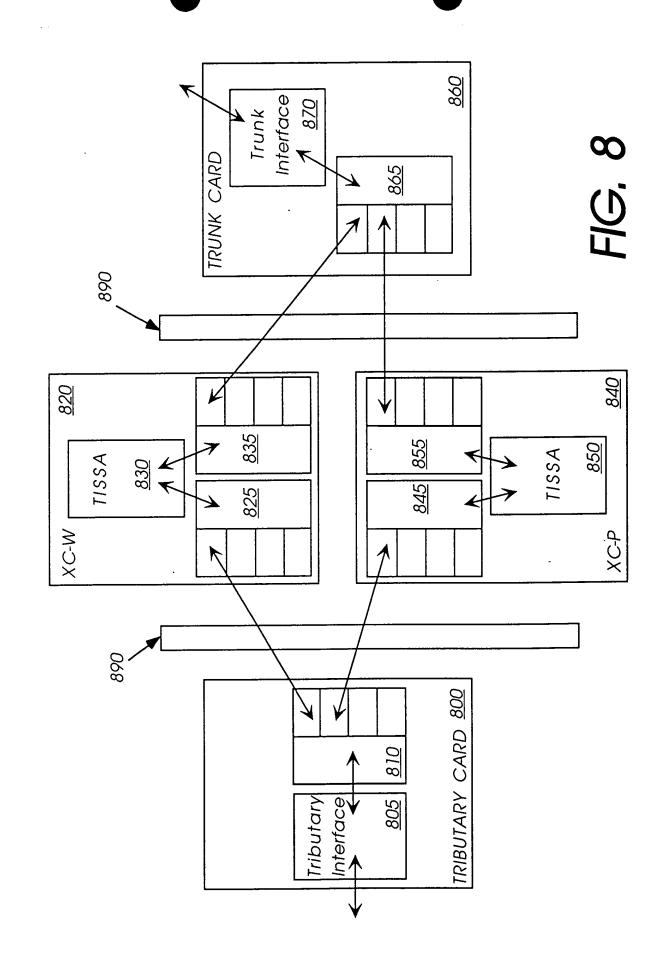


FIG. 5



F16. 6



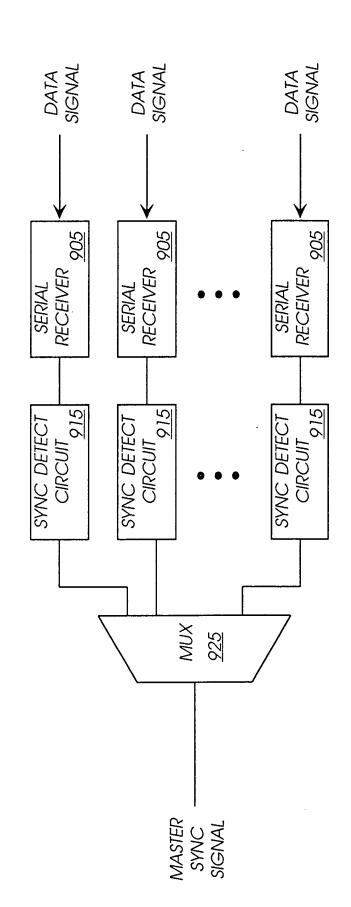
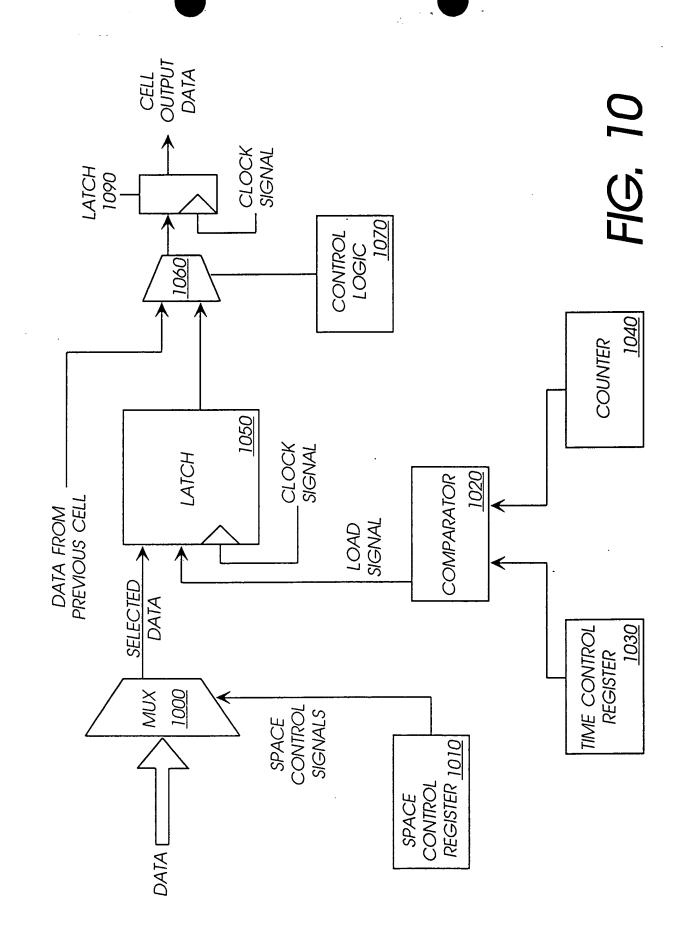
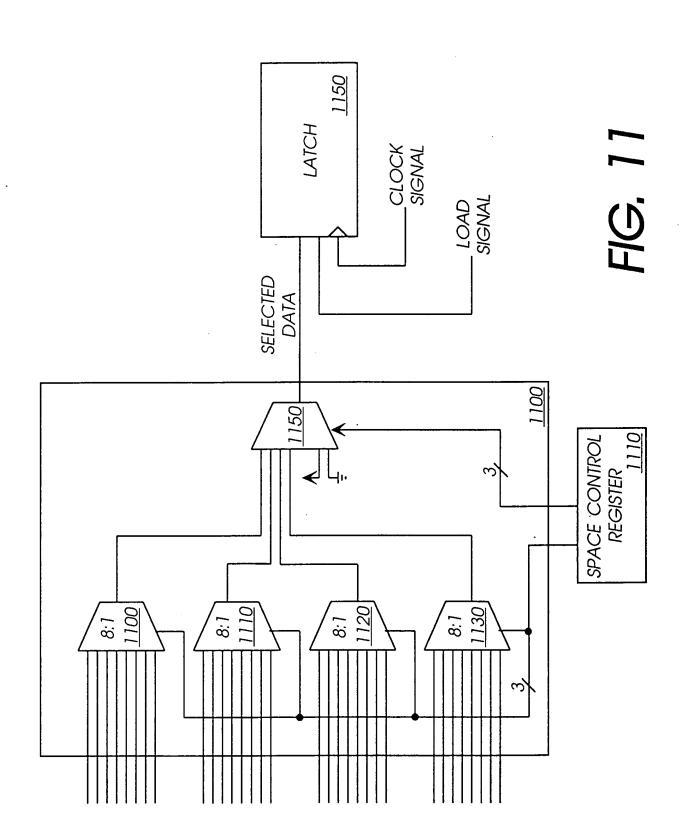


FIG. 9a

,											96
	915	920	935	940	955	<u>960</u>	<u>875</u>	<u>980</u>		066	FIG.
	$I_{X}$	$R_{X}$	$I_X$	$R_X$	$I_{X}$	$R_{X}$	$I_{\chi}$	$R_{X}$		61	
		0		0		0.		0.70			
		<u>910</u>		930		<u>950</u>		<u>970</u>	ų	J	
	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 0	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 1	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 2	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 3	- INTEREACE		•
006	12		2		92		92				





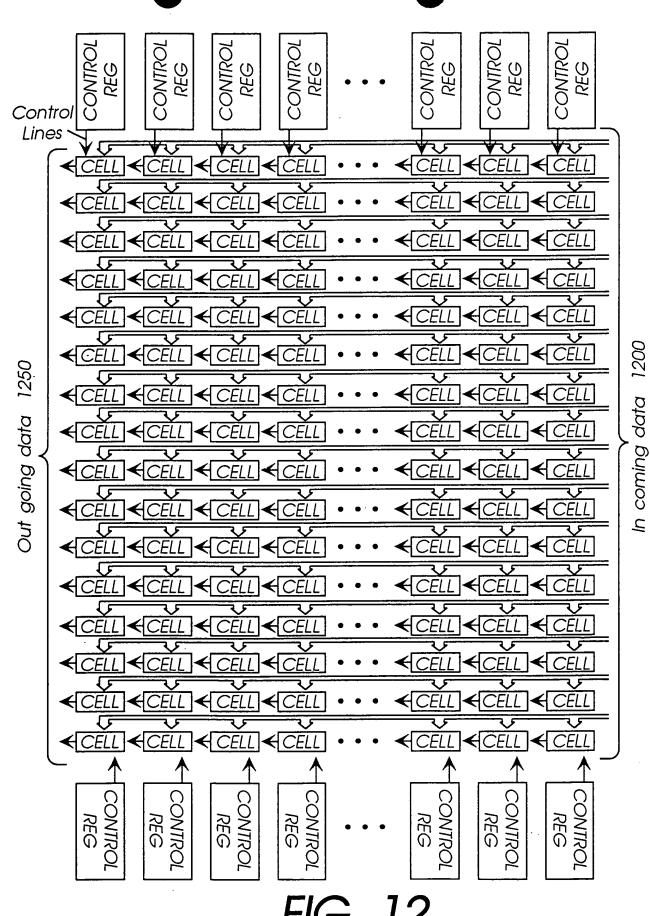


FIG. 12

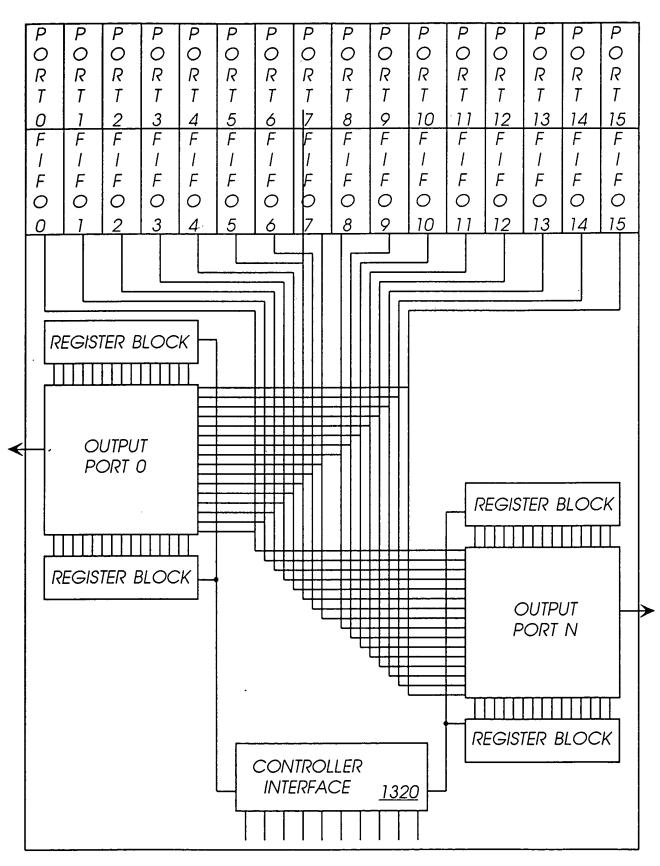


FIG. 13

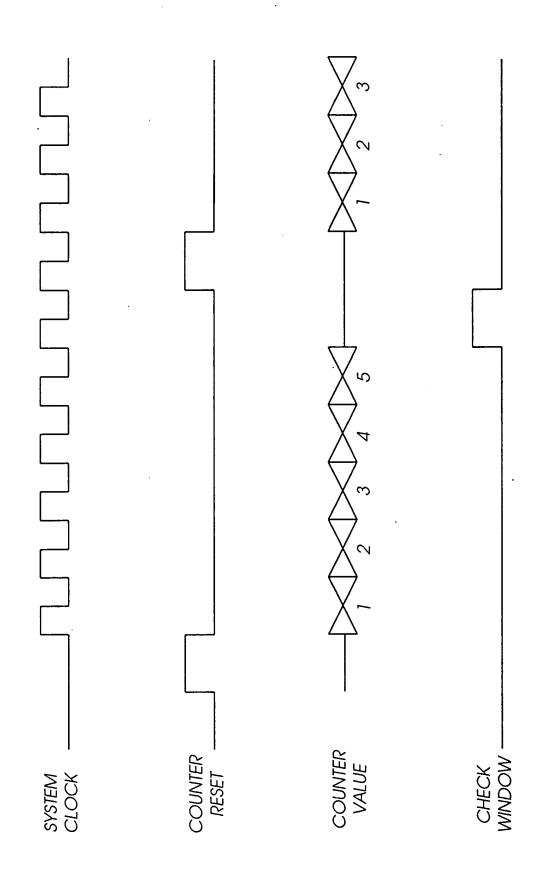


FIG. 14

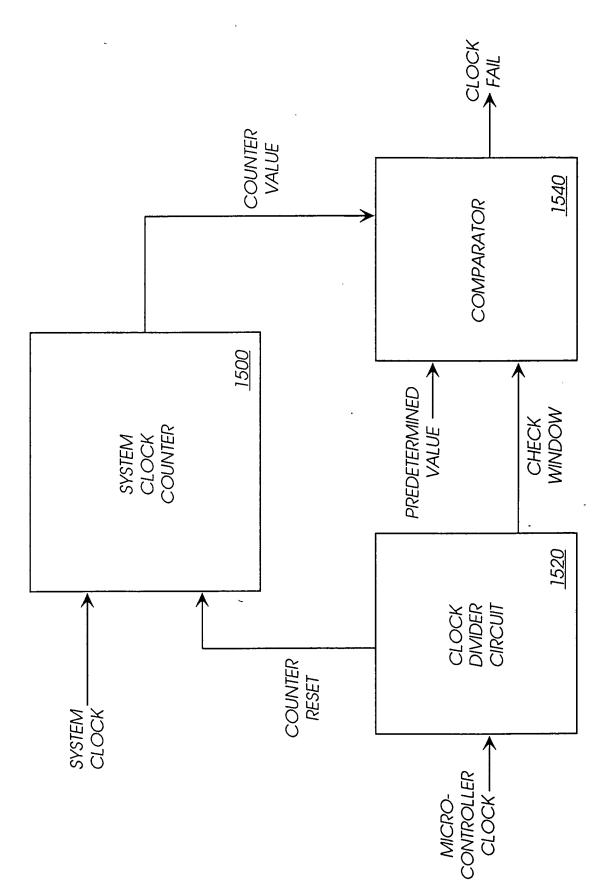


FIG. 15

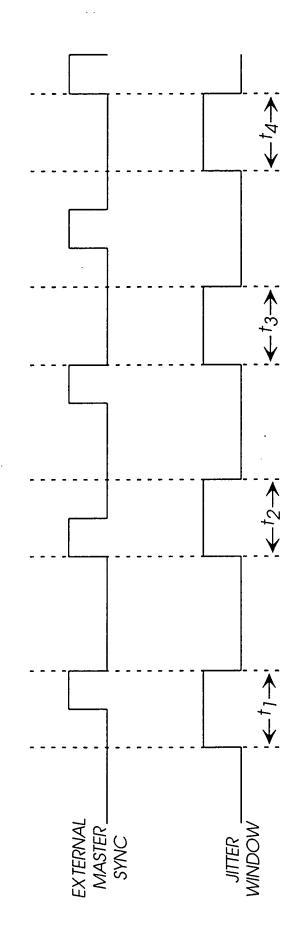
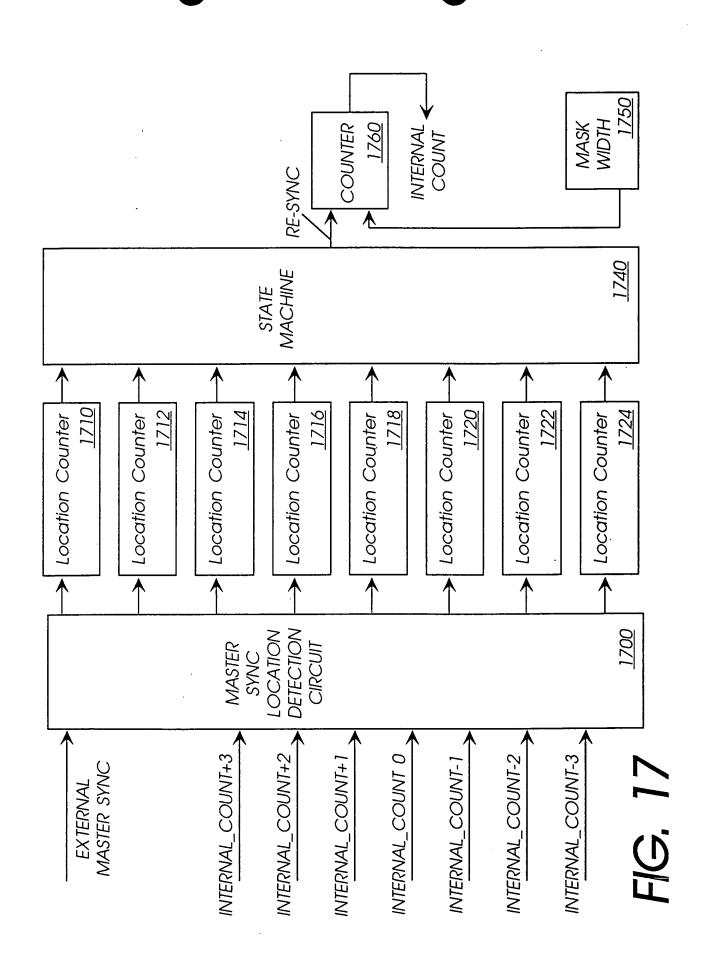


FIG. 16



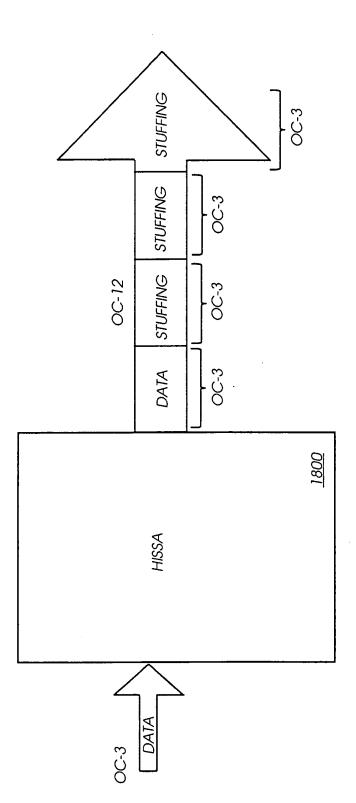


FIG. 18

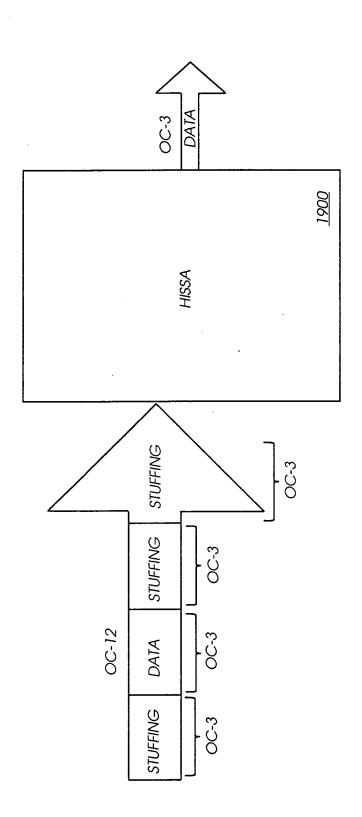
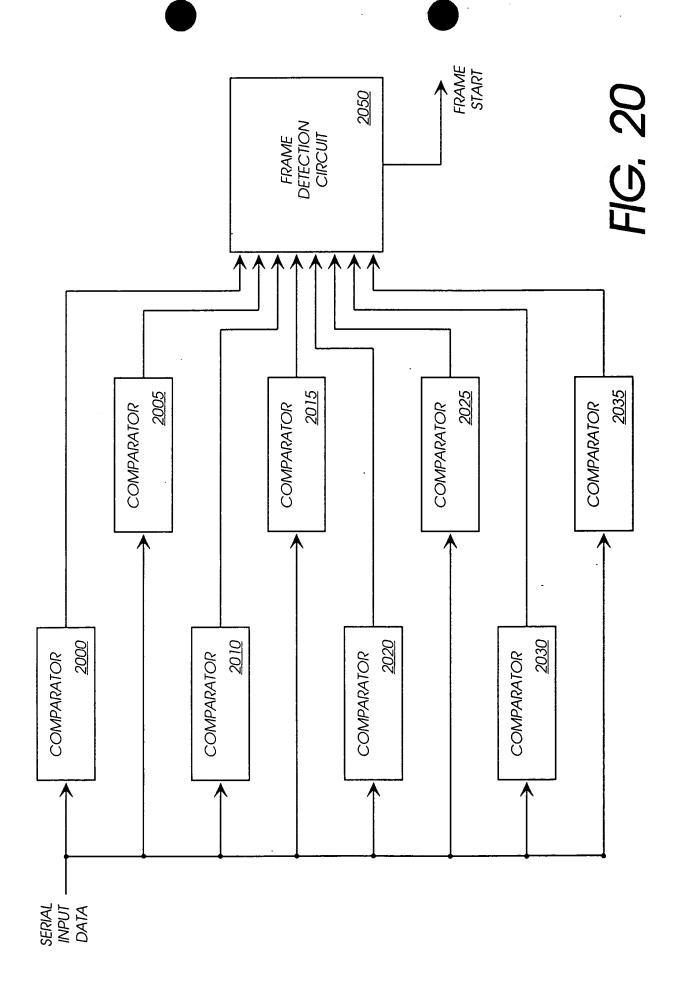


FIG. 19



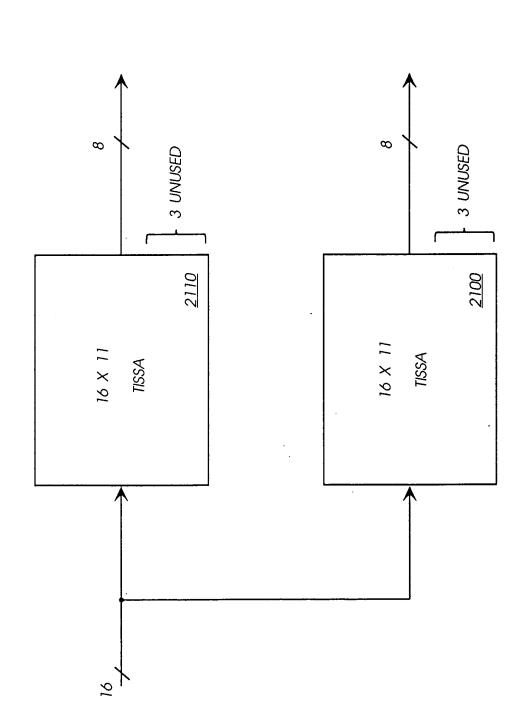
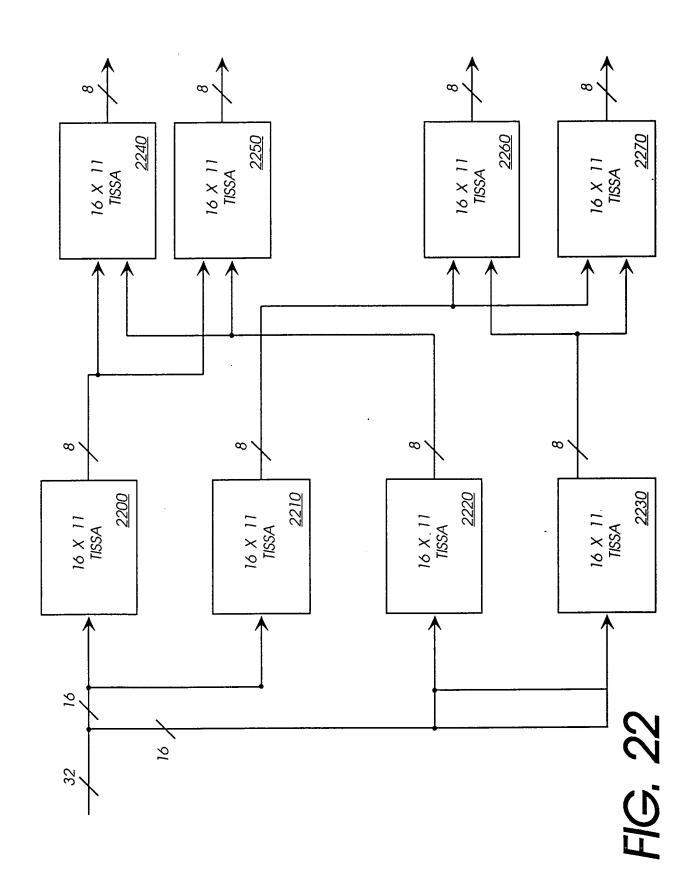


FIG. 21



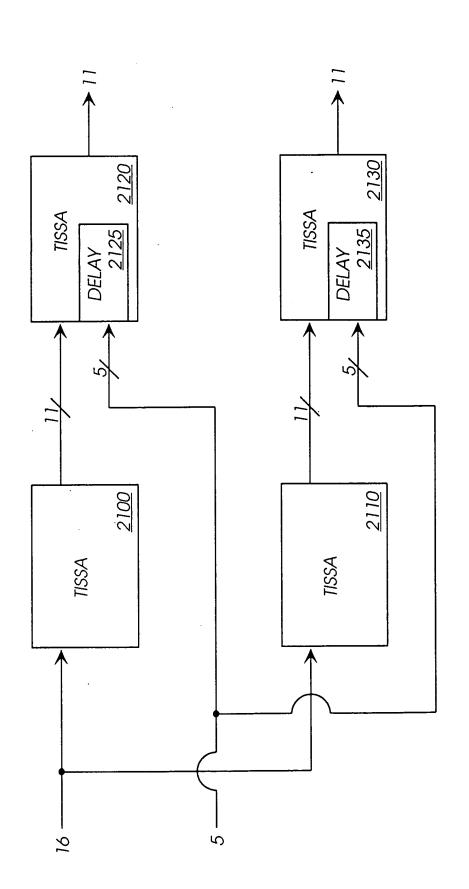


FIG. 23